Electronic Acknowledgement Receipt				
EFS ID:	2484448			
Application Number:	10622780			
International Application Number:				
Confirmation Number:	4253			
Title of Invention:	Circuit for detecting abnormal operation of memory and integrated circuit and method for detectiing abnormal operation			
First Named Inventor/Applicant Name:	Atsushi Yusa			
Customer Number:	20987			
Filer:	Andrew J. Telesz.			
Filer Authorized By:				
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1 Amendment - After Non-Final Rejection	OKI553_REQFORRECONSI D.pdf	259557	no	10	
		161173f64218e1a2221f65abbf0127508 33c2ce9			
Warnings:					
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2 Fee Worksheet (PTO-06)	fee-info.pdf	8174	no	2	
		6a9cae78eat545ee21c35e0tat66d92d8 92d4221			
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